

L Number	Hits	Search Text	DB	Time stamp
-	2	vid with fld	USPAT	2004/05/15 21:26
-	622	"power management" with (lc chip "Integrated circuit")	USPAT	2004/04/29 18:30
-	24	3.ab.	USPAT	2004/04/29 18:21
-	133	"power management" near (lc chip "Integrated circuit")	USPAT	2004/04/29 18:38
-	4	vid same fld	USPAT	2004/04/29 18:35
-	484	"power management" with cpu	USPAT	2004/04/29 18:36
-	19	("power management" with cpu) same (voltage and frequency)	USPAT	2004/04/29 18:36
-	191	"power management" adj (controller microcontroller)	USPAT	2004/04/29 18:38
-	33	("power management" adj (controller microcontroller)) same cpu	USPAT	2004/04/29 18:54
-	22	("power management" near (lc chip "Integrated circuit")) same cpu	USPAT	2004/04/29 18:55
-	2	(cpu same (vid "voltage id") and (fld "frequency id"))	USPAT	2004/04/30 10:28
-	11	(vid "voltage id") and (fld "frequency id")	USPAT	2004/04/30 10:29
-	74	cpu same (vid "voltage id")	USPAT	2004/04/29 19:04
-	3	"north bridge" with (voltage power) adj regulat\$3	USPAT	2004/04/29 19:32
-	42	"system controller" with (voltage power) adj regulat\$3	USPAT	2004/04/29 19:34
-	8	"north bridge" same (voltage power) adj regulat\$3	USPAT	2004/04/29 19:32
-	90	"system controller" same (voltage power) adj regulat\$3	USPAT	2004/04/29 19:34
-	9	(vid "voltage id") and (fld "frequency id")	US-PGPUB	2004/04/30 10:31
-	2111	chipset	USPAT	2004/04/30 10:41
-	117	chipset and "north bridge"	USPAT	2004/04/30 10:32
-	2376	((host north system) adj bridge "system controller") same frequency	USPAT	2004/04/30 10:59
-	7	((((host north system) adj bridge "system controller") same frequency) same "voltage regulator"	USPAT	2004/05/06 21:05
-	20	((((host north system) adj bridge "system controller") same frequency) same "power management"	USPAT	2004/04/30 12:00
-	549	Integrated adj2 monitoring	USPAT	2004/05/06 21:03
-	37	(Integrated adj2 monitoring) and (pcb "printed circuit board" motherboard chipset)	USPAT	2004/05/06 21:04
-	6	(Integrated adj2 monitoring) same (pcb "printed circuit board" motherboard chipset)	USPAT	2004/05/06 21:04
-	87	("north bridge" "memory controller hub" mch) same (voltage frequency)	USPAT	2004/05/06 21:15
-	46	("north bridge" "memory controller hub" mch) with power	USPAT	2004/05/07 11:41
-	14	(voltage power frequency) near2 regulat\$3 same ("north bridge" "memory controller hub" mch)	USPAT	2004/05/07 11:43
-	1362	vid "voltage id" "voltage Identification"	USPAT	2004/05/07 17:53
-	19	(vid "voltage id" "voltage Identification") with ("voltage regulator" vrm)	USPAT	2004/05/07 17:49
-	10	("5705920" "5774736" "6157974" "6185139" "6316988" "6340918" "6351827" "6425086" "6525599" "6600302").PN.	USPAT	2004/05/07 17:26
-	5079	fld "frequency id" "frequency Identification"	USPAT	2004/05/07 17:38
-	5	(vid "voltage id" "voltage Identification") same ("voltage regulator" vrm) not ((vid "voltage id" "voltage Identification") with ("voltage regulator" vrm))	USPAT	2004/05/07 17:28
-	17	(vid "voltage id" "voltage Identification") and (fld "frequency id" "frequency Identification")	USPAT	2004/05/07 17:46
-	20530	"north bridge" "system controller" "memory controller hub" mch	USPAT	2004/05/07 17:35
-	577	vid "voltage id" "voltage Identification"	US-PGPUB	2004/05/07 17:38
-	2670	fld "frequency id" "frequency Identification"	US-PGPUB	2004/05/07 17:51
-	11	(vid "voltage id" "voltage Identification") and (fld "frequency id" "frequency Identification")	US-PGPUB	2004/05/07 17:38
-	1	(vid "voltage id" "voltage Identification") and cpuld	USPAT	2004/05/07 17:43
-	0	(fld "frequency id" "frequency Identification") and cpuld	USPAT	2004/05/07 17:44

-	3	((vid "voltage Id" "voltage Identification") (fld "frequency Id" "frequency Identification")) same (processor cpu) adj Interface	USPAT	2004/05/07 17:45
-	10	(vld "voltage Id" "voltage Identification") same ("north bridge" "system controller" "memory controller hub" mch)	USPAT	2004/05/07 17:46
-	55	(vld "voltage Id" "voltage Identification") and ("north bridge" "system controller" "memory controller hub" mch)	USPAT	2004/05/07 17:49
-	4	(fld "frequency Id" "frequency Identification") same ("north bridge" "system controller" "memory controller hub" mch)	USPAT	2004/05/10 16:12
-	102	(fld "frequency Id" "frequency Identification") and ("north bridge" "system controller" "memory controller hub" mch)	USPAT	2004/05/07 17:49
-	58	("north bridge" "system controller" "memory controller hub" mch) same ("voltage regulator" vrm)	USPAT	2004/05/07 17:50
-	11	vld "voltage Id" "voltage Identification"	IBM_TDB	2004/05/07 17:52
-	41	fld "frequency Id" "frequency Identification"	IBM_TDB	2004/05/07 17:52
-	164	vld "voltage Id" "voltage Identification"	JPO	2004/05/07 17:52
-	440	fld "frequency Id" "frequency Identification"	JPO	2004/05/07 17:53
-	0	(vld "voltage Id" "voltage Identification") and (fld "frequency Id" "frequency Identification")	JPO	2004/05/07 17:52
-	0	((vld "voltage Id" "voltage Identification") (fld "frequency Id" "frequency Identification")) and cpuld	JPO	2004/05/07 17:52
-	32	vld "voltage Id" "voltage Identification"	EPO	2004/05/07 17:53
-	183	fld "frequency Id" "frequency Identification"	EPO	2004/05/07 17:53
-	0	((vld "voltage Id" "voltage Identification") (fld "frequency Id" "frequency Identification")) and cpuld	EPO	2004/05/07 17:53
-	0	"vld{4:0}"	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/07 17:54
-	0	"fld{3:0}"	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/07 17:54
-	23	overrid\$3 near4 (frequency voltage) same (cpu processor)	USPAT	2004/05/11 12:17
-	73	"core voltage" same (cpu processor)	USPAT	2004/05/10 16:22
-	68	"core voltage" with (cpu processor)	USPAT	2004/05/10 16:22
-	500	vcc2	USPAT	2004/05/11 14:06
-	10	10.ab.	USPAT	2004/05/11 14:03
-	17	10.clm.	USPAT	2004/05/11 14:03
-	4	vcc2det	USPAT	2004/05/11 14:06
-	35	vcc2 same cpu	USPAT	2004/05/11 14:08
-	3	(cpuld cpu-Id) and (vld "voltage Id" "voltage Identification")	USPAT	2004/05/11 14:15
-	12	dynamlc\$4 near4 (core cpu) near2 voltage	USPAT	2004/05/11 14:13
-	55	(decod\$3 translat\$3) with (vld "voltage Id" "voltage Identification")	USPAT	2004/05/11 16:03
-	33	overclock\$3	USPAT	2004/05/12 16:38
-	993	over adj clock\$3	USPAT	2004/05/12 16:39
-	0	(over adj clock\$3) adj controller	USPAT	2004/05/12 16:39
-	1	(over adj clock\$3) adj (chip lc)	USPAT	2004/05/12 16:41
-	15	(over adj clock\$3) with "Integrated circuit"	USPAT	2004/05/13 11:02
-	0	attanslc.as.	USPAT; US-PGPUB; EPO; JPO	2004/05/13 11:04
-	53	(lte "Integrated technology express").as.	USPAT; US-PGPUB; EPO; JPO	2004/05/13 11:10
-	1	(voltage frequency vld fld) with (Jumperless "Jumper free")	USPAT	2004/05/13 11:13
-	2	(voltage frequency vld fld) same (Jumperless "Jumper free")	USPAT	2004/05/13 11:20
-	22	soft adj (voltage frequency vld fld)	USPAT	2004/05/13 11:19
-	0	(voltage frequency vld fld) same (Jumperless "Jumper free")	EPO; JPO; IBM_TDB	2004/05/13 11:19

-	2	soft adj (voltage frequency vld fld)	EPO; JPO; IBM_TDB	2004/05/13 11:20
-	0	(voltage frequency vld fld) same "on the fly"	USPAT	2004/05/13 11:20
-	0	(voltage frequency vld fld) same "on the fly"	EPO; JPO; IBM_TDB	2004/05/13 11:20
-	6	(voltage frequency vld fld) with "dynamic scaling"	USPAT	2004/05/16 19:58
-	18	5745375.URPN.	USPAT	2004/05/13 11:23
-	0	6484265.URPN.	USPAT	2004/05/13 11:44
-	70	(voltage frequency) near2 scaling with (chip ic "Integrated circuit")	USPAT	2004/05/13 13:21
-	6438	vld fld voltage adj (ld Identification) frequency adj (ld Identification)	USPAT	2004/05/16 20:00
-	25	(vld fld voltage adj (ld Identification) frequency adj (ld Identification)) with dynamic	USPAT	2004/05/16 20:01
-	0	(vld fld voltage adj (ld Identification) frequency adj (ld Identification)) with "on the fly"	USPAT	2004/05/16 20:02
-	0	(vld fld voltage adj (ld Identification) frequency adj (ld Identification)) with (Jumperless "Jumper free")	USPAT	2004/05/16 20:02
-	1255	(cpu processor core) adj (voltage frequency)	USPAT	2004/05/16 21:37
-	10	((cpu processor core) adj (voltage frequency)) with dynamic	USPAT	2004/05/16 21:36
-	0	((cpu processor core) adj (voltage frequency)) with "on the fly"	USPAT	2004/05/16 21:37
-	0	(cpu processor core) with "on the fly"	USPAT	2004/05/16 21:43
-	723	sleep with frequency	USPAT	2004/05/16 21:42
-	1	sleep with (core cpu processor) adj frequency	USPAT	2004/05/16 21:42
-	3	sleep same (core cpu processor) adj frequency	USPAT	2004/05/16 21:42
-	163	(sleep with frequency) same (cpu processor computer)	USPAT	2004/05/16 21:43
-	19	sleep same (fld frequency adj (ld Identification))	USPAT	2004/05/16 21:44
-	627	(cpu computer processor) same (fld frequency adj (ld Identification))	USPAT	2004/05/16 21:45
-	32	22.ab.	USPAT	2004/05/16 21:45